other.

Detailed Description Text (63):
Consequently, the display data stored in the frame memory of the multi-display interface 901 identified by the ID number 3 is updated; that is, when the effective display data 1003 shown in FIG. 10(c) is transferred through the display data buses 103 and 105, only the liquid crystal panel 102-3 not in a display pause state displays display data 1103-3 corresponding to the effective display data 1003 shown in FIG. 10(c) as shown in FIG. 11(b).

Detailed Description Text (72):
The display data 1302 shown in FIG. 13(a) is transferred through the display data bus 103 and the display data 1304 shown in FIG. 13(b) is transferred through the display data bus 1202. As shown in FIG. 14, the display data selecting registers 1205 of the multi-display interfaces 1201 connected to the liquid crystal panels 102-1 and 102-3 identified respectively by the ID numbers 1 and 3 select the display data 1302 shown in FIG. 13(a) and transferred through the display data bus 103.

Detailed Description Text (73):
Similarly, the display data selecting registers 1205 of the multi-display interfaces 1201 connected to the liquid crystal panels 102-2 and 102-4 identified respectively by the ID numbers 2 and 4 select the display data 1304 shown in FIG. 13(b) and transferred through the display data bus 1202.